

2019/2020 Timetable

Module timetable - Circuit Design & Electronic Testing (30/09/2019 - 14/09/2020)

	08:00 09:00	09:00 10:00	10:00 11:00	11:00 12:00	12:00 13:00	13:00 14:00	14:00 15:00	15:00 16:00	16:00 17:00	17:00 18:00	18:00 19:00	19:00 20:00	20:00 21:00
Thu						Lecture, 30/01/2020 ... 07/05/2020 ENG774 (Circuit Design & Electronic Testing) ; B 20; E15 Lab							