

Module timetable - Circuit Design Analysis & Testing

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Thu 23/03/2023		Lecture, 09:30AM-12:00PM, Wk 34 Module: ENG790 (Circuit Design Analysis & Testing) Staff: Sharp, Andrew Room: <u>E2</u>			g)	

Module timetable - Circuit Design Analysis & Testing

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Thu 30/03/2023			Lecture, 09:30AM-12:00PM, Wk 35 Module: ENG790 (Circuit Design Analysis & Testing) Staff: Sharp, Andrew Room: E2								

Module timetable - Circuit Design Analysis & Testing

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Mo 03/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 36										
Tue 04/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 36										
We 05/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 36										
Thu 06/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 36										
Fri 07/04/2023	Easter Break - Good Friday Unavailable, 08:00AM-09:00PM, Wk 36										
Sat 08/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 36										
Sun 09/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 36										

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	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Mo 10/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 37										
Tue 11/04/2023	Easter - University Closed Unavailable, 08:00AM-09:00PM, Wk 37										
We 12/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 37										
Thu 13/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 37										
Fri 14/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 37										
Sat 15/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 37										
Sun 16/04/2023	Easter Break Unavailable, 08:00AM-09:00PM, Wk 37										

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	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Thu 20/04/2023			Lecture, 09:30AM-12:00PM, Wk 38 Module: ENG790 (Circuit Design Analysis & Testing) Staff: Sharp, Andrew Room: <u>E2</u>								

Module timetable - Circuit Design Analysis & Testing

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Thu 27/04/2023			Lecture, 09:30AM-12:00PM, Wk 39 Module: ENG790 (Circuit Design Analysis & Testing) Staff: Sharp, Andrew Room: <u>E2</u>								

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	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Mo 01/05/2023	Bank Holiday Unavailable, 08:00AM-09:00PM, Wk 40										
Thu 04/05/2023			Lecture, 09:30AM-12:00PM, Wk 40 Module: ENG790 (Circuit Design Analysis & Testing) Staff: Sharp, Andrew Room: E2								

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	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Mo 08/05/2023	Bank Holiday Unavailable, 08:00AM-09:00PM, Wk 41										
Thu 11/05/2023			Lecture, 09:30AM-12:00PM, Wk 41 Module: ENG790 (Circuit Design Analysis & Testing) Staff: Sharp, Andrew Room: <u>E2</u>								

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	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Thu 25/05/2023		Online Exam, 09:30AM-12:30PM, Wk 43 Module: ENG790 (Circuit Design Analysis & Testing) Staff: Sharp, Andrew									

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	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Mo 29/05/2023	Bank Holiday Unavailable, 08:00AM-09:00PM, Wk 44										

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM
Mo 28/08/2023	Bank Holiday Unavailable, 08:00AM-09:00PM, Wk 3										